

# 2019-03-21 Meeting notes

## Date

21 Mar 2019

## Attendees

- Casey Cain
- Daniel Pono Takamori (pono) LF
- VM (Vicky) Brasseur (she/her) (Juniper)
- Edward Ting (Lenovo)
- Prabhjot Singh Sethi (ATS)
- Joseph Gasparakis (Intel)
- Sukhdev Kapur (Juniper)
- Valentin Sinitsyn (Yandex)
- Darien Hirotsu (Redapt)
- Jim St. Leger (Intel)

## Agenda

- Start the Recording
- [Antitrust Policy](#)
- Agenda Bashing (Roll Call, Action Items (5 minutes))
- General Topics
  - [ARB](#)
  - CLA Status
- Release
- Blueprint review process
- Jira discussion
- event planning (which events are we gathering?)
- [tsc@lists.tungsten.io](mailto:tsc@lists.tungsten.io) works?
- TSC Chair election status

## Minutes

- CLA
  - Casey Cain heard from LF CLA team
  - Needed to make more changes by request of LF Legal
  - In theory should now be live by next Friday (March 29th)
  - Hopefully ccain will have access to CLA admin console by tomorrow (fingers crossed)
    - Start testing on his side
  - Will keep folks up to date
- tsc@?
  - Edward Ting isn't receiving email for this
  - Seems to be caught in moderation?
  - ccain is clearing that queue
  - Joseph: first email to the list is always moderated
    - So this is sorted now that his message is approved
  - SK expresses concerns about the speed of the moderation process
    - ccain assures folks that the queue gets checked daily
- Release (standing topic that's a part of the template)
  - What's up with 5.1...?
  - Still in the works
  - SK needs to know the plan for releases once they move to the community
    - VMB will send him & the tsc@ list the doc folks have been working on about that
- Blueprint review process
  - Sukhdev Kapur Asks if we can have a streamlined process for approving blueprints.
  - ccain lists out a proposed process
  - JG: that's similar to in the past but is it documented anywhere?
    - Also, discussion of these rarely takes only 10 minutes
  - SK walks through some existing contrail blueprint/spec templates
    - [https://github.com/Juniper/contrail-specs/blob/master/blueprint\\_template.md](https://github.com/Juniper/contrail-specs/blob/master/blueprint_template.md)
    - [https://github.com/Juniper/contrail-specs/blob/master/design\\_template.md](https://github.com/Juniper/contrail-specs/blob/master/design_template.md)
  - The existing Jira for Blueprints: <https://jira.tungsten.io/projects/TFB/issues/TFB-1384?filter=allopenissues>
- ARB
  - Suggested that the ARB isn't needed, replace with PTLs?
  - Randy isn't here to speak on the subject
  - SK is A-OK with considering this, but stresses that will need to discuss this in context of using PTLs/core committers appropriately (per governance doc)
    - Were leaning on ARB because there weren't any PTLs

- ccain suggests maybe need to have another look at the community governance doc & perhaps rev it?
  - ARB & PTLs need to be addressed
  - Release process, too
  - Lots of work to do, after we put out other fires (like the infra migration & CLAs)
- SK suggests do this as part of repo migration
  - Declare some defactor core committers at that time
  - Bootstrap the PTLs in this way
  - Can do PTL elections later after things settle in a bit
  - Wouldn't need ARB if there are PTLs
- VMB is generally +1 on the idea but stresses must ensure core/PTLs are aware of & accept the responsibilities
- Will continue the discussion on list and in next call

## Action items

- ✓ Casey Cain will email VM (Vicky) Brasseur (she/her) to add Mirantis CCLA to the existing system
- ✓ VM (Vicky) Brasseur (she/her) will email link to release planning doc/wiki page to tsc@
- ✓ Daniel Pono Takamori will work with Sukhdev & Casey to get existing blueprint & spec contrail templates moved to Jira & modified accordingly
- ✓ Casey Cain will send an email asking for CCLA admins to send their LF IDs ASAP
- ✓ Sukhdev Kapur will start the thread on tsc@ about core committers/PTLs